

# PROJECT / Analog Predistorter in Integrated Circuit

## APIC

### Main Objective:

- Analysis of implementation approaches for Analog Pre-Distorters (APDs) for power amplifier linearization, under the foreseen 5G scenario;
- Design and implementation of a new proposed APD based on an IC chip to be manufactured within the project, implementing configurable functions through analog basic building blocks.

Reference: , Funding: IT, Start Date: 01-07-2016

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